

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 010481	SERIAL NO. Unknown
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		APPLICANT Dunworth, et al.	
		FILING DATE March 1, 2002	GROUP Unknown
DATE MAILED: <u>March 6, 2002</u>			

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPRO- PRIATE
	A1						
	A2						
	A3						
	A4						
	A5						
	A6						
	A7						
	A8						
	A9						
	A10						

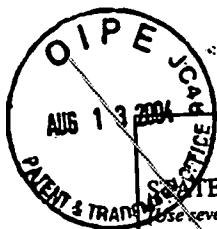
FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS
	B1						
	B2						
	B3						

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)

807	C1	Wilson, et al., "A CMOS Self-Calibrating Frequency Synthesizer", IEEE Journal of Solid-State Circuits, Vol. 35, No. 10, October 2000, pps 1437 - 1444.
074	C2	Lin, et al., "A 900-MHz 2.5-mA CMOS Frequency Synthesizer with an Automatic SC Tuning Loop", IEEE Journal of Solid-State Circuits, Vol. 36, No. 3, March 2001, pps. 424 - 431.
EXAMINER <i>[Signature]</i>		DATE CONSIDERED <u>1/7/05</u>

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



STATEMENT BY APPLICANT
(Use several sheets if necessary)

DATE MAILED: August 9, 2004

ATTY. DOCKET NO.
010481

SERIAL NO.
10/092,669

APPLICANT
Dunworth, et al.

FILING DATE
3/6/02

GROUP
Unknown

2685

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
814	A1	2 0 0 2 0 0 7 9 9 7 3	6/27/02	Higashi, et al.			11/20/01
824	A2	2 0 0 2 0 0 3 6 5 4 5	3/28/02	Fridi, et al			8/13/01

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	A3	5 6 0 4 4 6 5	2/18/97	Farabaugh			
	A4	5 1 8 2 5 2 8	01/26/93	Zuta			
	A5						

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AUG 18 2004

FOREIGN PATENT DOCUMENTS

Technology Center 2600

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS
814	B1	1 1 6 8 6 2 7	01/02/02	EPO	Matsushita Electric, etc., et al.		
824	B2	9 9 0 4 4 9 5	01/28/99	WO	Microchip, etc., et al.		
	B3						
	B4						

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)

	C1	
	C2	
	C3	
EXAMINER	DATE CONSIDERED 01/07/05	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Sheet 2 of 3

Sheet 2 of 3

FORM PTO-1219 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE				ATTY. DOCKET NO. 010481		SERIAL NO. 10/092,669	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				APPLICANT Dunworth, et al.		FILING DATE March 6, 2002	
DATE MAILED: 11/22/04				GROUP 2682		2685	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPRO- PRIATE
SP	A12	4 0 1 8 0 8 1	1/18/00	O'Shaughnessy			
	A19	4 1 2 3 7 9 7	10/17/00	Leontice, et al.			
	A21	4 0 5 4 2 4 7	2/15/00	Shu et al.			
	A23	4 1 2 0 8 2 1	11/21/00	Wellins, et al.			
	A22	4 1 2 7 1 2 8	12/22/00	Mauerman			
	A24	4 1 7 2 3 8 0	1/10/01	Lloyd, et al.			
	A25	4 7 2 2 8 2	1/15/01	Yanaka			
	A26	4 2 1 1 7 4 7	4/2/01	Trishet et al.			
	A27	4 4 7 3 6 0 7	10/29/02	Shohara et al.			
	A28	4 4 8 8 7 4 5	11/28/02	Manhara et al.			
A29	4 2 4 2 0 4 4	4/1/02	Samuliat et al.				
FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS
B1							
B2							
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)							
EXAMINER	C1	DATE CONSIDERED					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809; Draw line through citation if not in conformance and not amended. Include copy of this form with next communication to applicant.							

Sheet 1 of 3

FORM PTO-1219 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE				ATTY. DOCKET NO. 010481		SERIAL NO. 10/092,669	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				APPLICANT Dunworth, et al.		FILING DATE March 6, 2002	
DATE MAILED: 11/22/04				GROUP 2682		2685	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPRO- PRIATE
SP	A1	4 0 8 7 7 3 0	2/2/78	Allen, et al.			
	A2	4 2 1 3 2 0 2	1/25/82	Bruckner, et al.			
	A3	4 4 2 4 4 8 2	9/12/84	Bayler			
	A4	4 7 2 2 0 8	12/20/88	Pynd, et al.			
	A5	4 2 1 0 3 7 3	2/3/90	Niles			
	A6	4 0 4 0 2 0	7/10/90	Heldrich			
	A7	4 0 2 3 5 7 2	6/11/91	Caldwell, et al.			
	A8	4 0 7 2 3 2 2	1/7/92	Gron, et al.			
	A9	4 1 0 0 8 2	2/22/92	Grimm, et al.			
	A10	4 2 2 2 3 6 2	8/2/94	Bellandone, et al.			
	A11	4 2 8 2 2 2 2	1/17/92	Gorsbach, et al.			
	A12	4 4 7 2 2 8 5	12/2/92	Zenki, et al.			
	A13	4 4 2 3 7 1 0	2/20/96	Takahara et al.			
	A14	4 2 8 2 2 9 0	10/27/98	Gunn			
	A15	4 8 2 1 4 8 2	11/2/99	Nidoni, et al.			
	A16	4 8 7 0 0 0 1	2/5/99	Guendine, et al.			
	A17	4 2 3 0 3 1 2	7/20/99	Park			
	A18	4 0 1 6 0 8 1	1/18/00	O'Shaughnessy			
FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS
B1	2 2 6 8 0 7 8		10/15/93	JP	Nara Osamu		
B2	2 0 0 1 2 3 0 6 6 7		8/24/91	JP	Kobayashi Hideo et al.		
B3	0 4 2 2 9 4 1		2/12/90	EPO	Marconi Instruments Limited		
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)							
EXAMINER	C1	"A Low-Noise Multiple Output Phase Locked Loop Synthesizer for Radar Applications" European Frequency and Time Forum (2002)					
EXAMINER	C2	"Design of an Integrated CMOS PLL Frequency Synthesizer" IEEE					
DATE CONSIDERED							